

Fig. 1 PRIOR ART

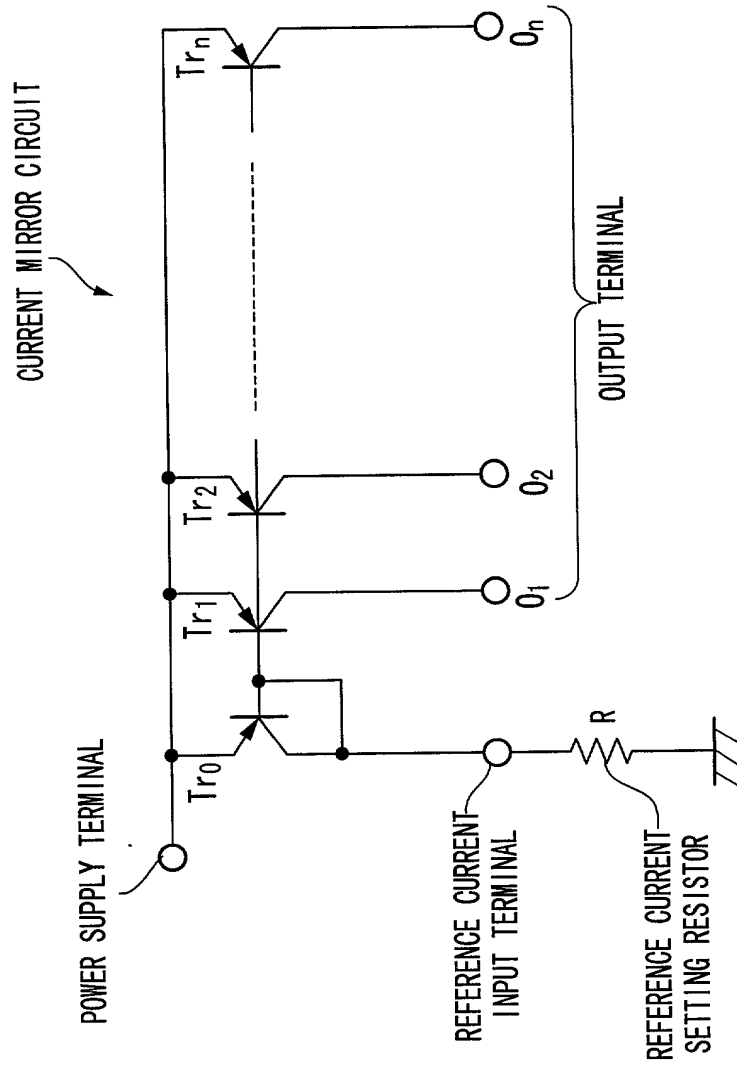


Fig. 2 PRIOR ART

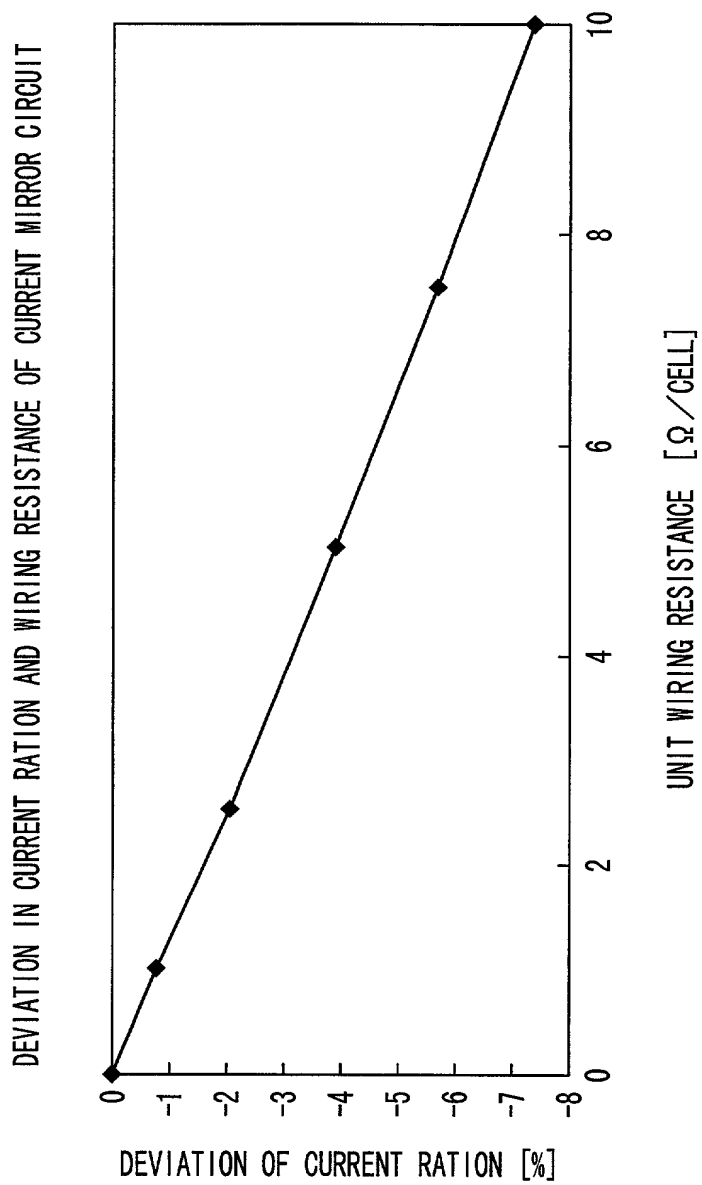


Fig. 3A PRIOR ART

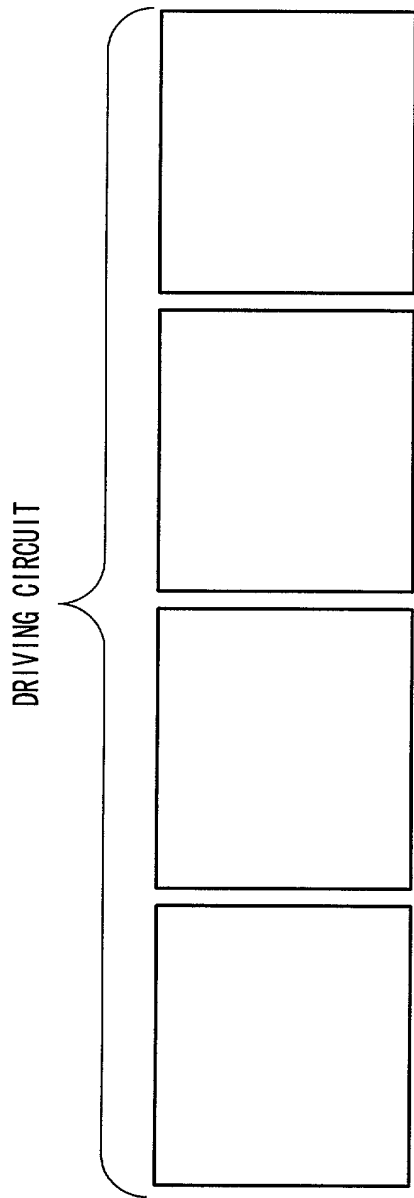


Fig. 3B PRIOR ART

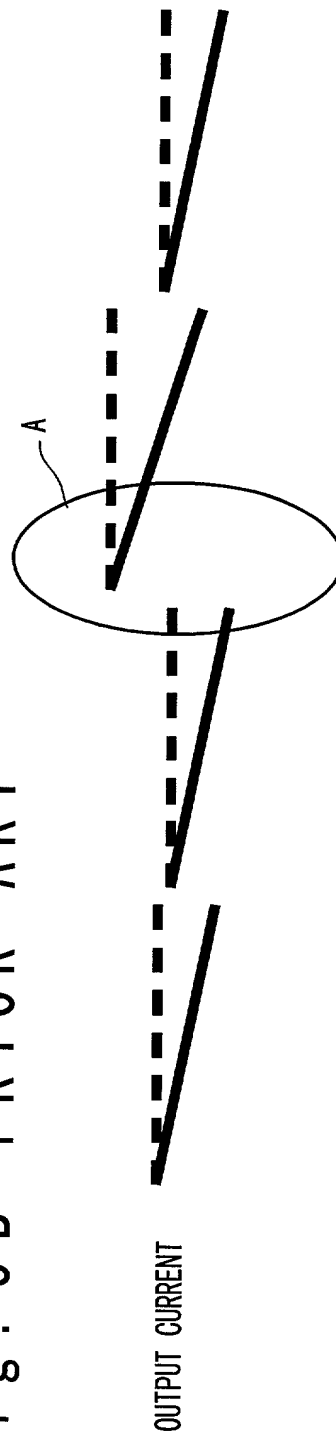


Fig. 4

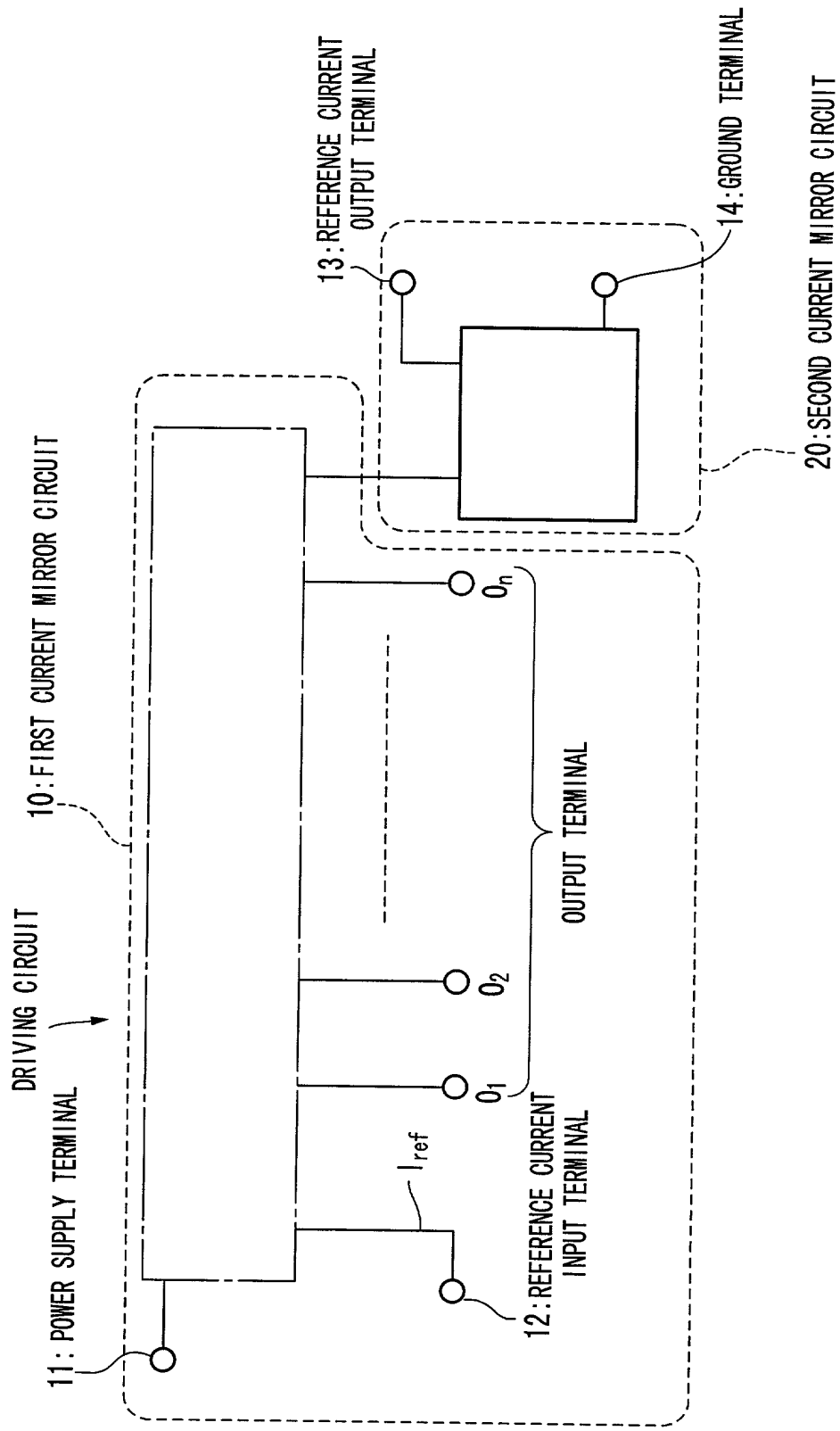


Fig. 5

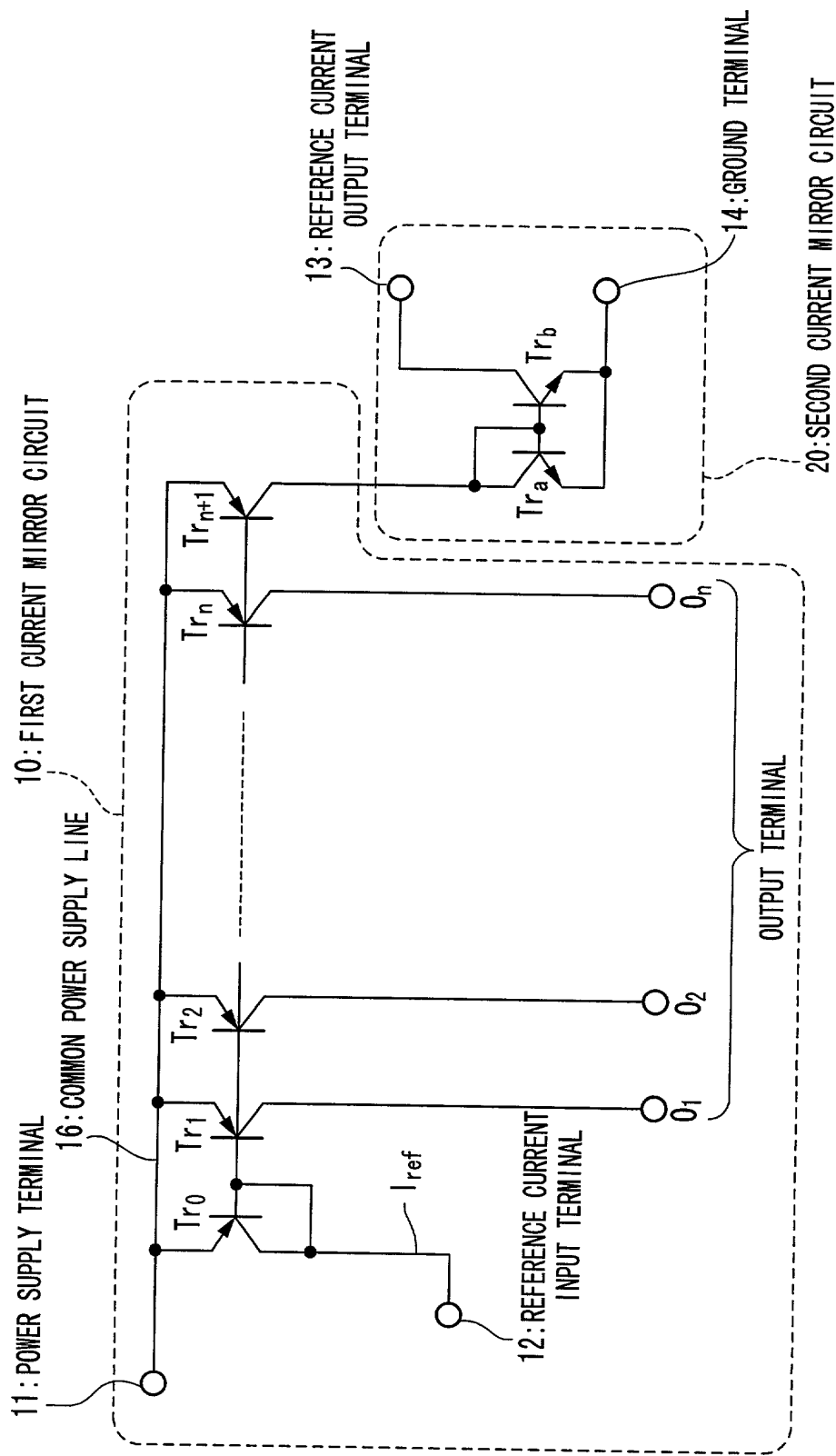


Fig. 6 A

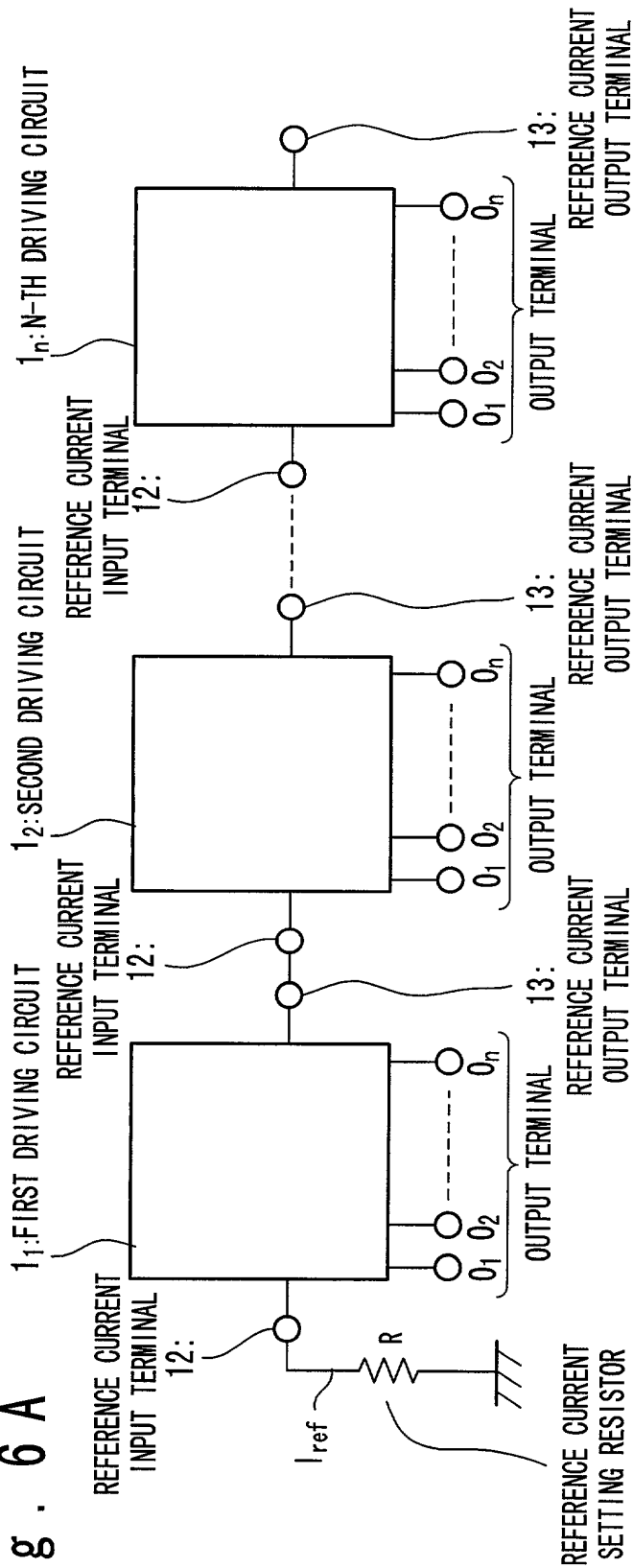


Fig. 6 B

OUTPUT CURRENT

Fig. 6 C

OUTPUT CURRENT

7-50

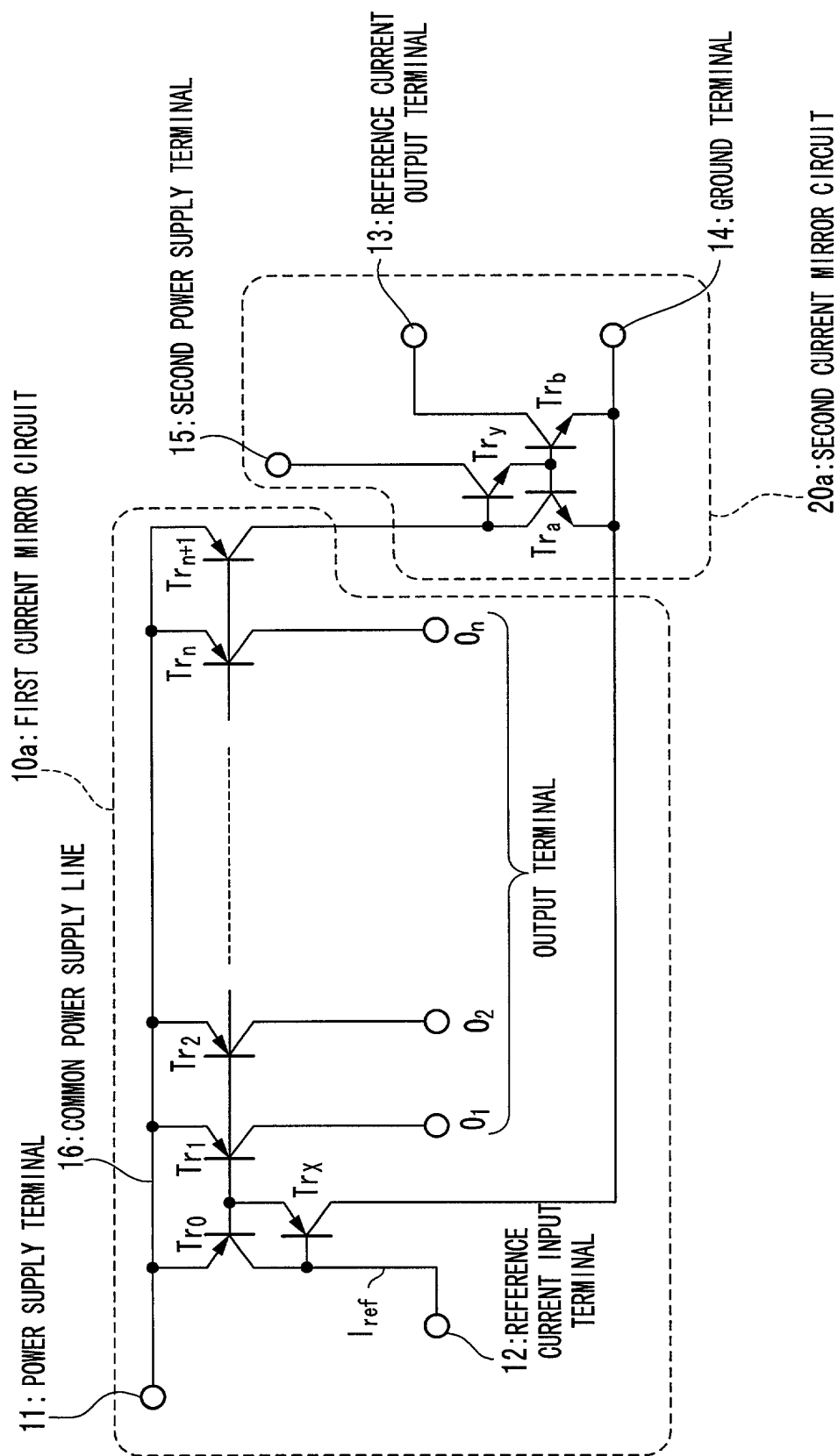


Fig. 8

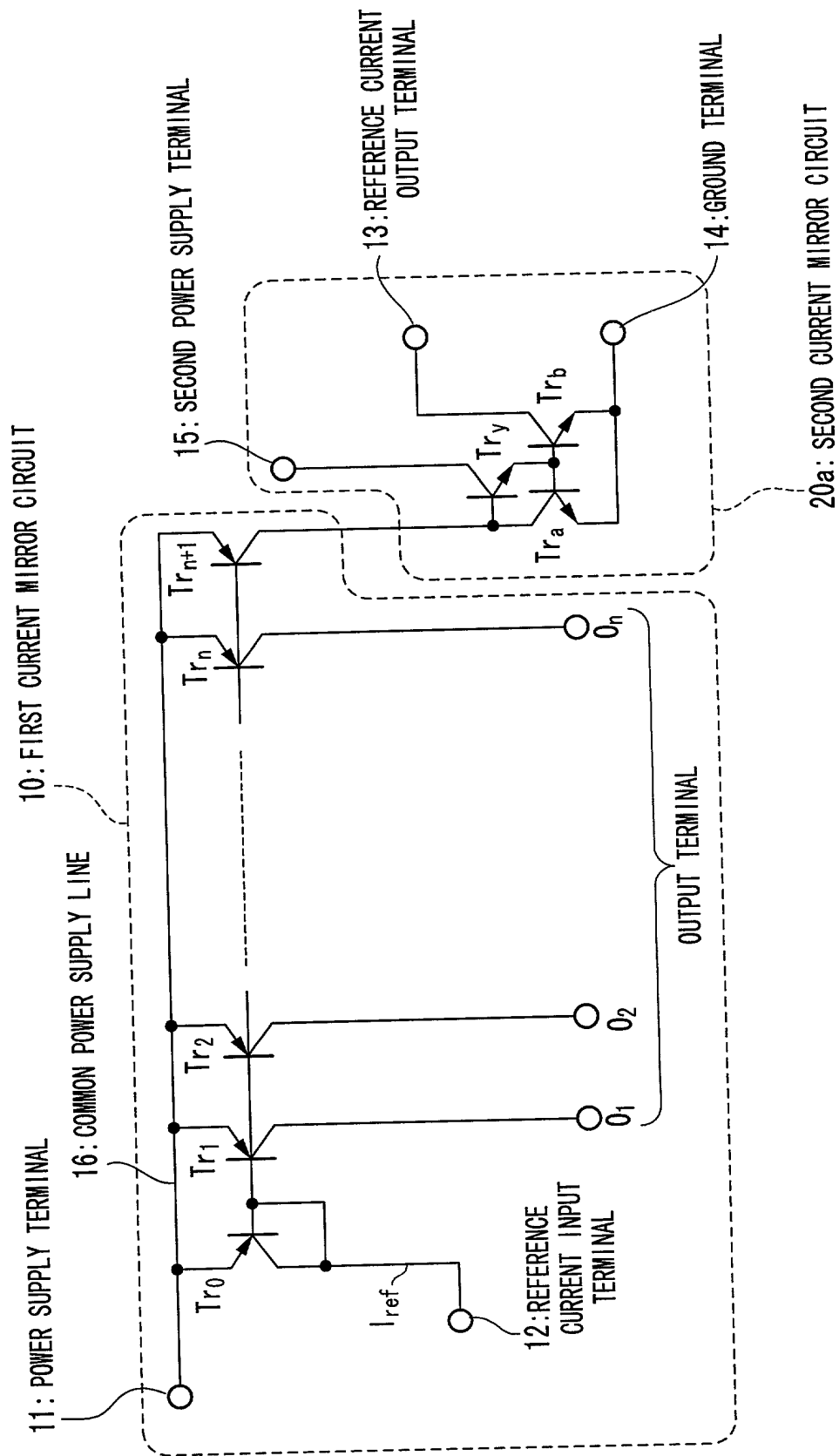
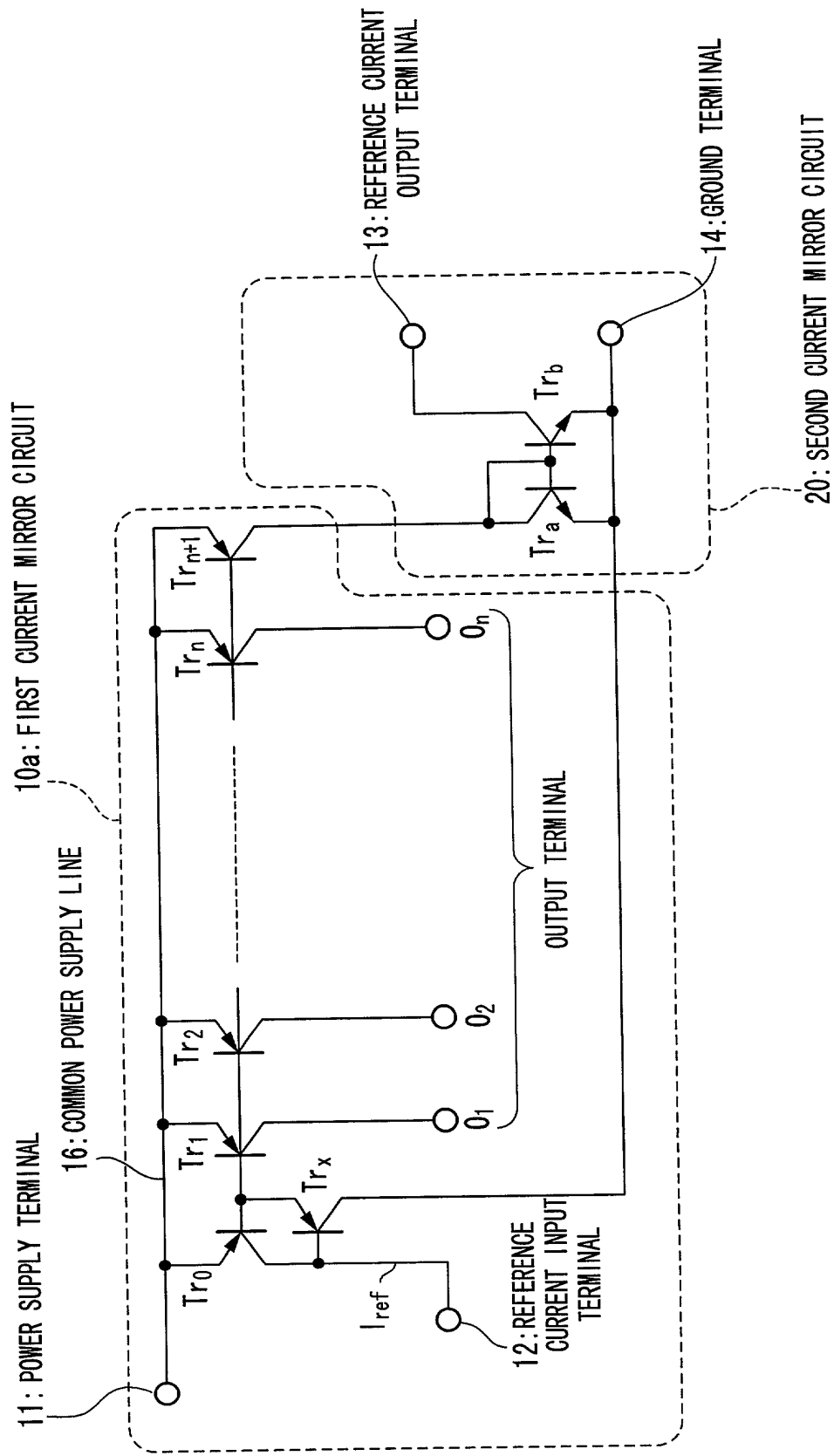




Fig. 9



10.15.11

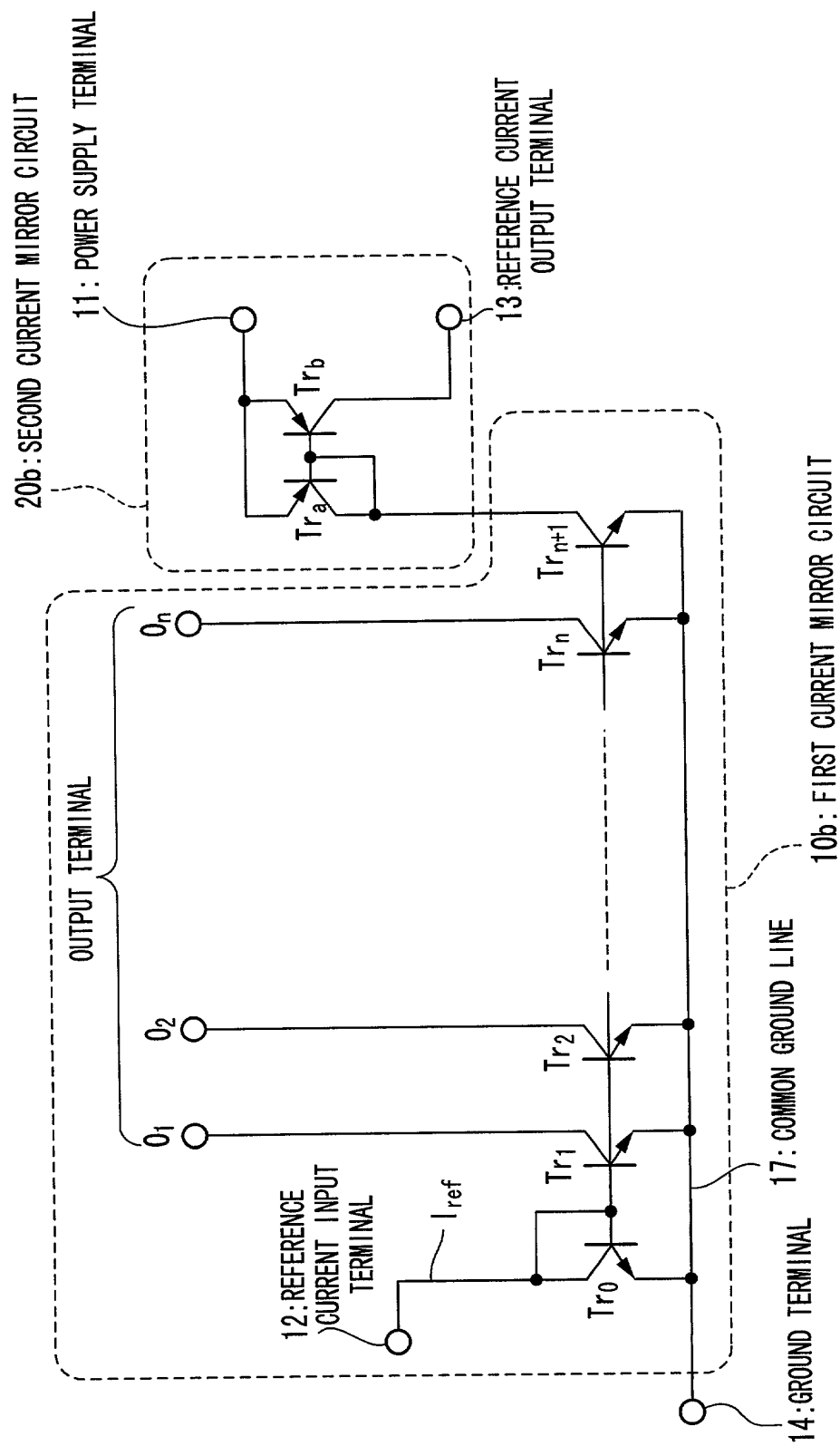




Fig. 12

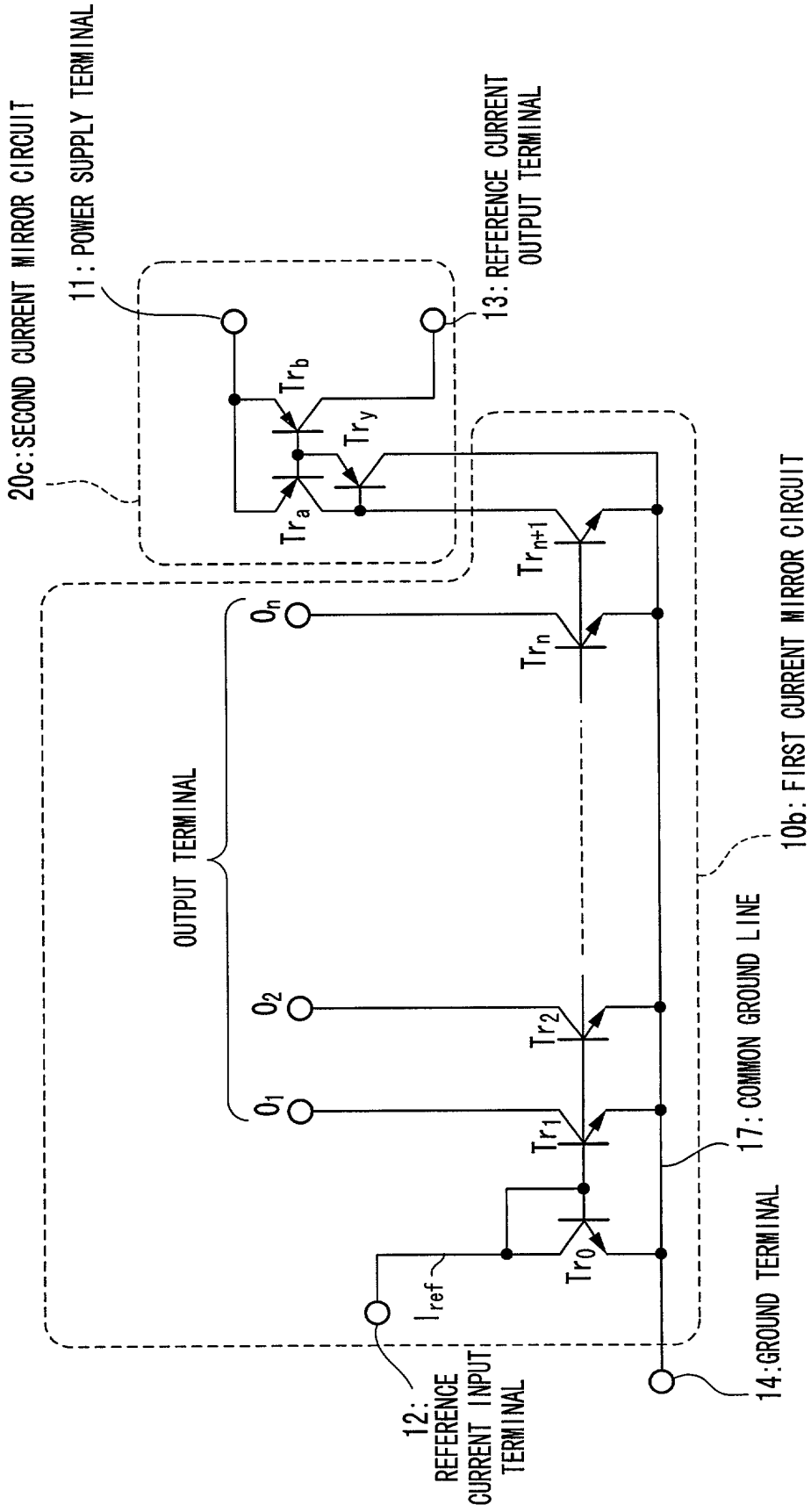


Fig. 13

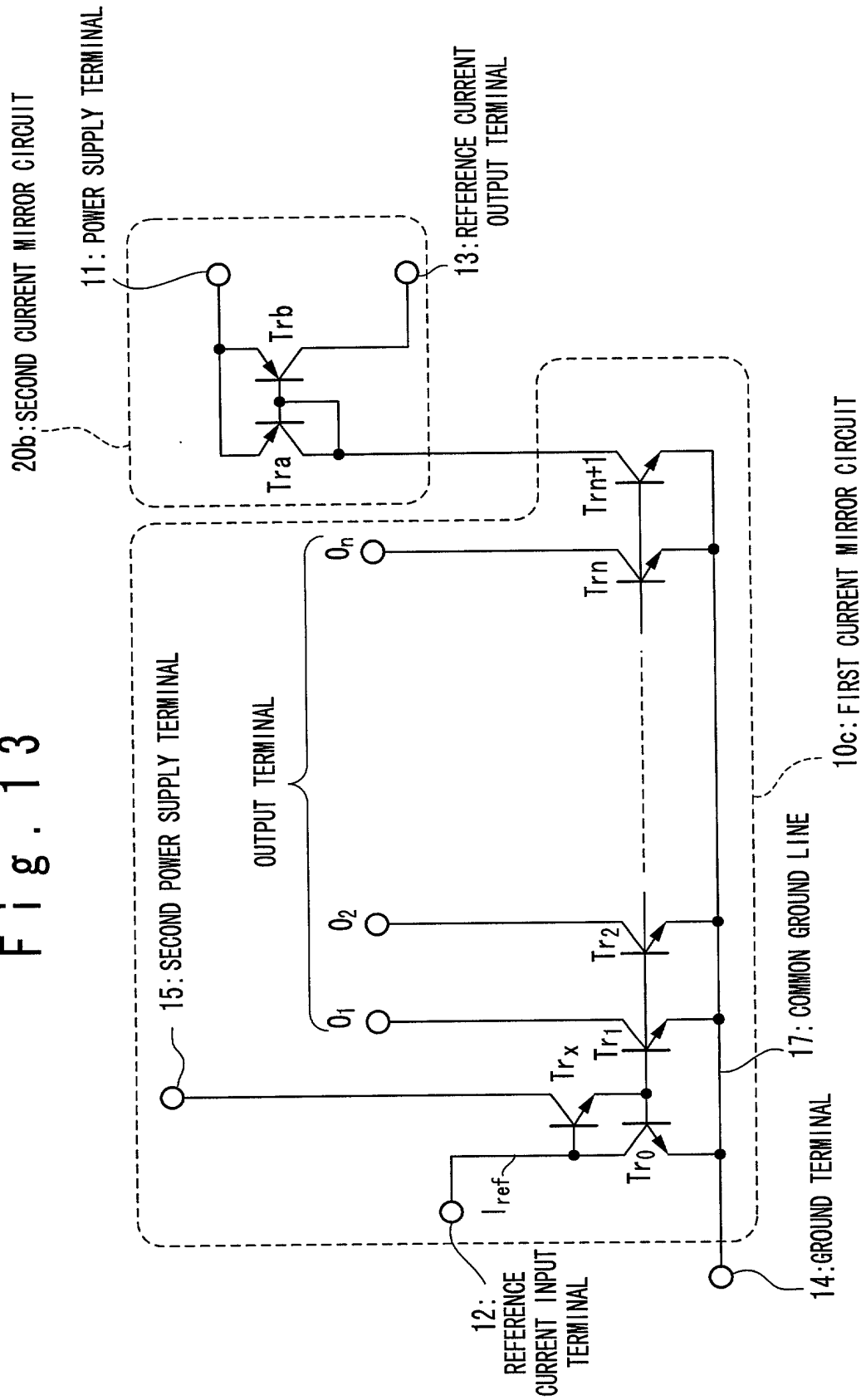
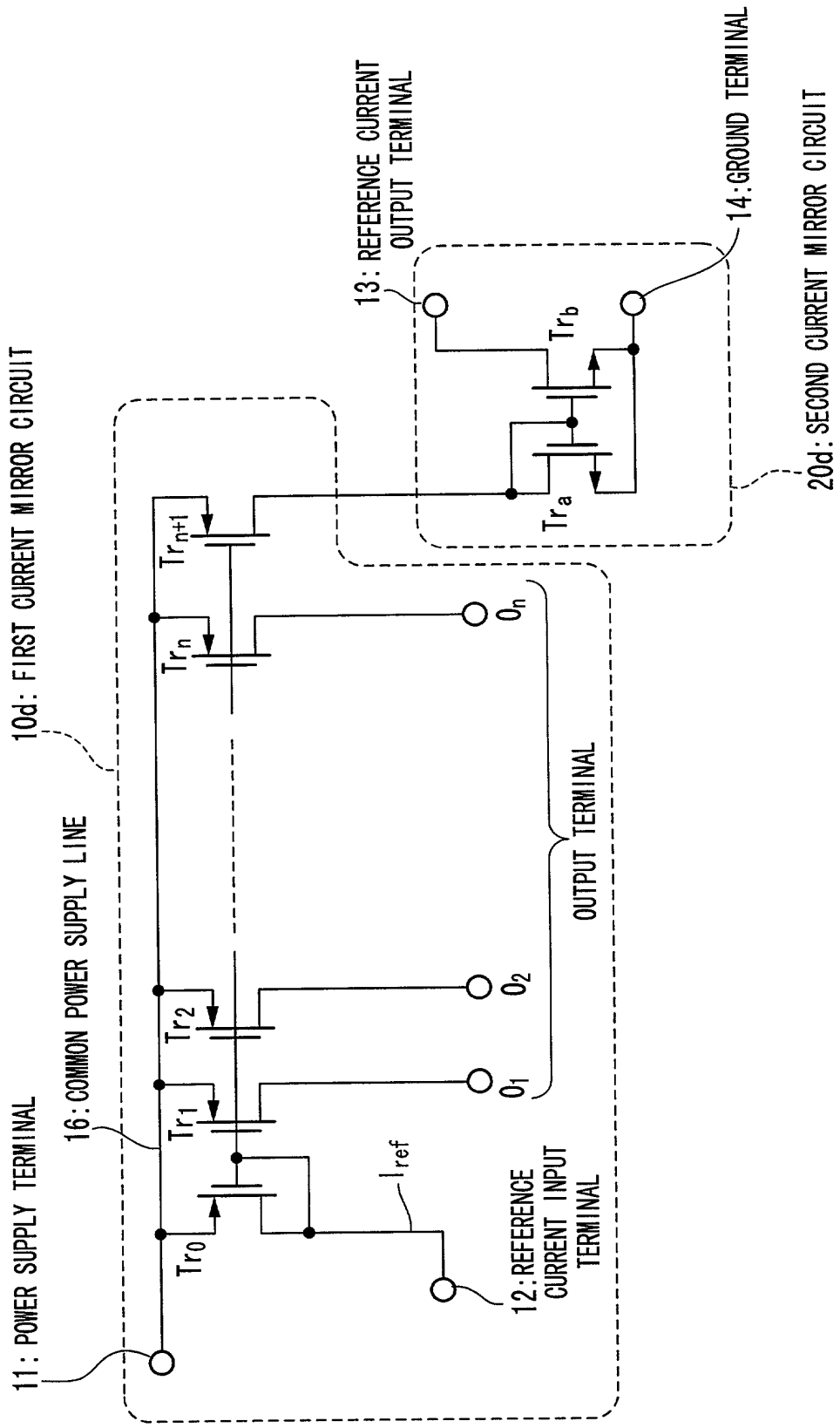


Fig. 14



Fi 15.

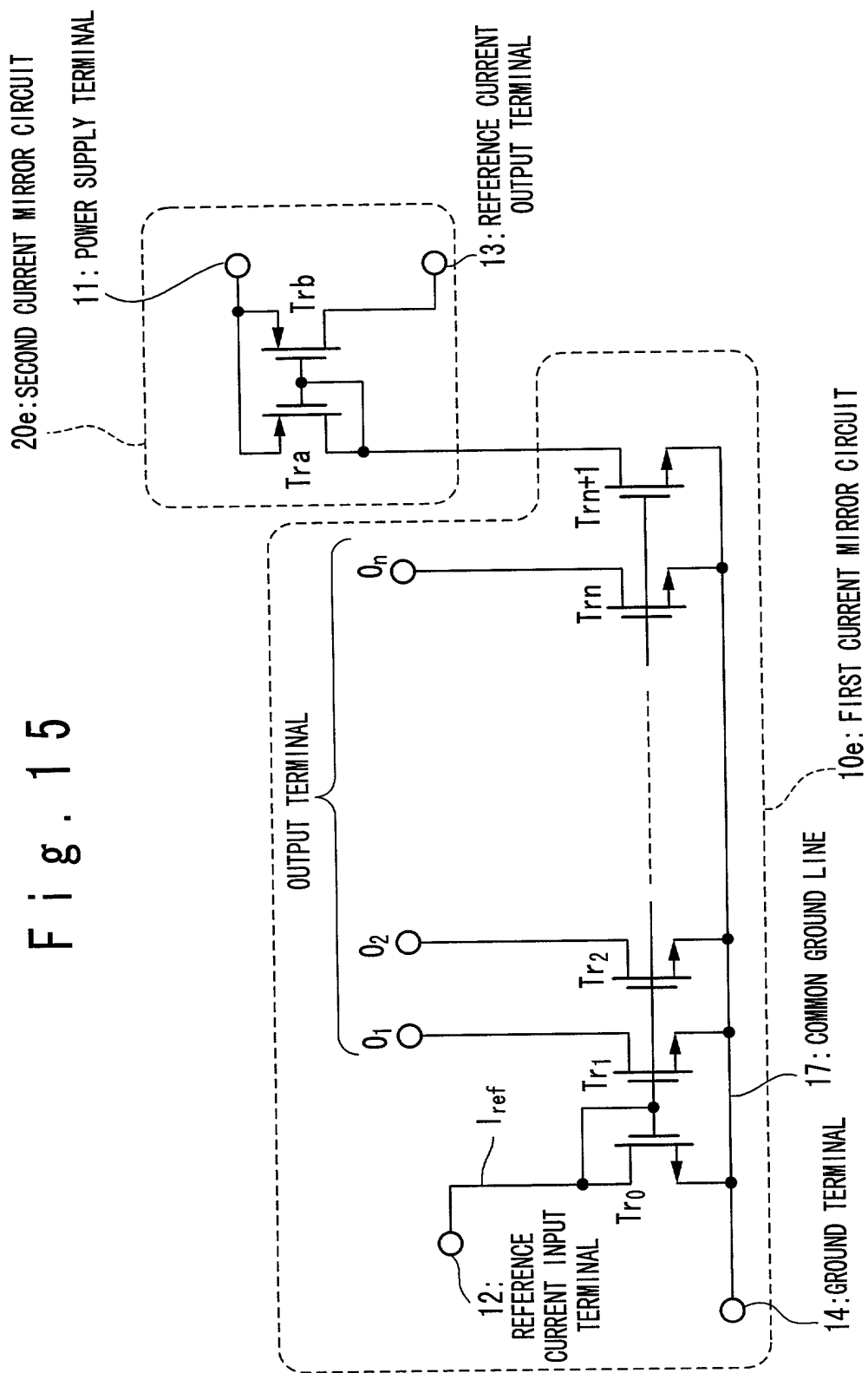


Fig. 16

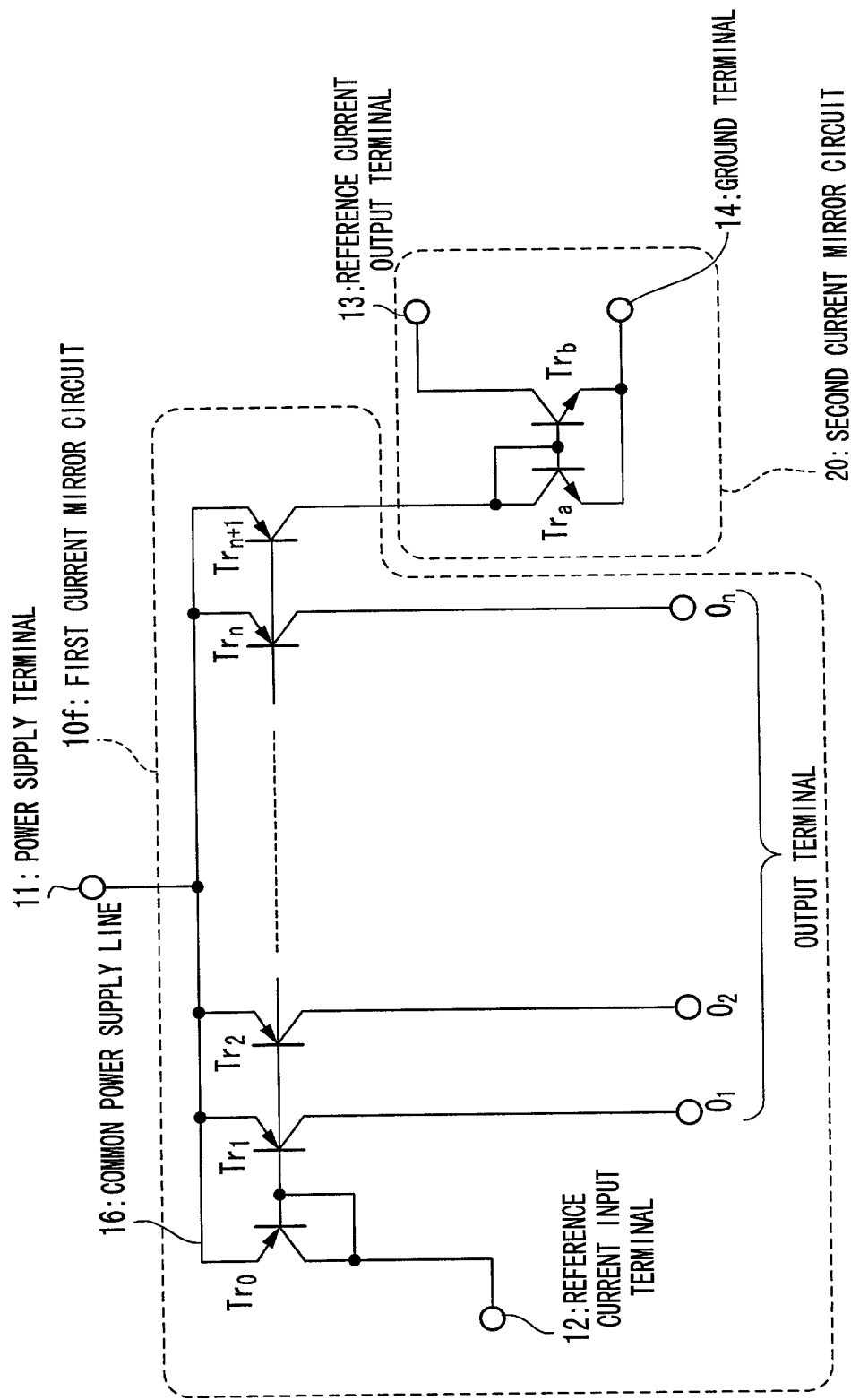




Fig. 17

